

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In application of: **Levine et al.**

Serial No.: **10/674,606**

Filed: **September 30, 2003**

For: **Method and Apparatus to  
Autonomically Take an Exception on  
Specified Instructions**

**35525**

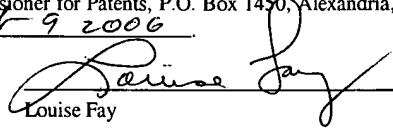
PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

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Group Art Unit: **2183**

Examiner: **Fiegle, Ryan Paul**

Attorney Docket No.: **AUS920030485US1**

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Louise Fay

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Commissioner for Patents  
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Alexandria, VA 22313-1450

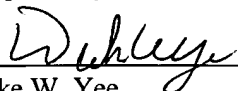
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- Supplemental Information Disclosure Statement;
- Form PTO-1449;
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Respectfully submitted,



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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**Levine et al.**

Serial No: **10/674,606**

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Title: **Method and Apparatus to  
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Specified Instructions**

Group Art Unit: **2183**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
11400 Burnet Road  
Austin, Texas 78758

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
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Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

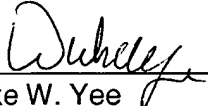
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. 1.97(g) the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (b) exists.

No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

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Respectfully submitted,

  
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|----------------------|-------------------|
| Application Number   | 10/674,606        |
| Filing Date          | 09/30/2003        |
| First Named Inventor | Levine et al.     |
| Examiner Name        | Fiegle, Ryan Paul |
| Art Unit             | 2183              |
| Attorney Docket No.  | AUS920030485US1   |

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

[illegible]

## FOREIGN PATENT DOCUMENTS

| Examiner Initial | Foreign Patent Document | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines, Where<br>Relevant Passages or Relevant<br>Figures Appear |
|------------------|-------------------------|--------------------------------|--|---|
|                  |                         |                                |  |   |

## NON PATENT LITERATURE

|                         |   |   |
|-------------------------|---|---|
| <b>Examiner Initial</b> | Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |   |
|                         | <b>AC</b>   | <b>Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.</b> |
|                         |   |   |

Examiner:

Date Considered:

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